

FIG. 1 is a block diagram of a digital signal processing system.

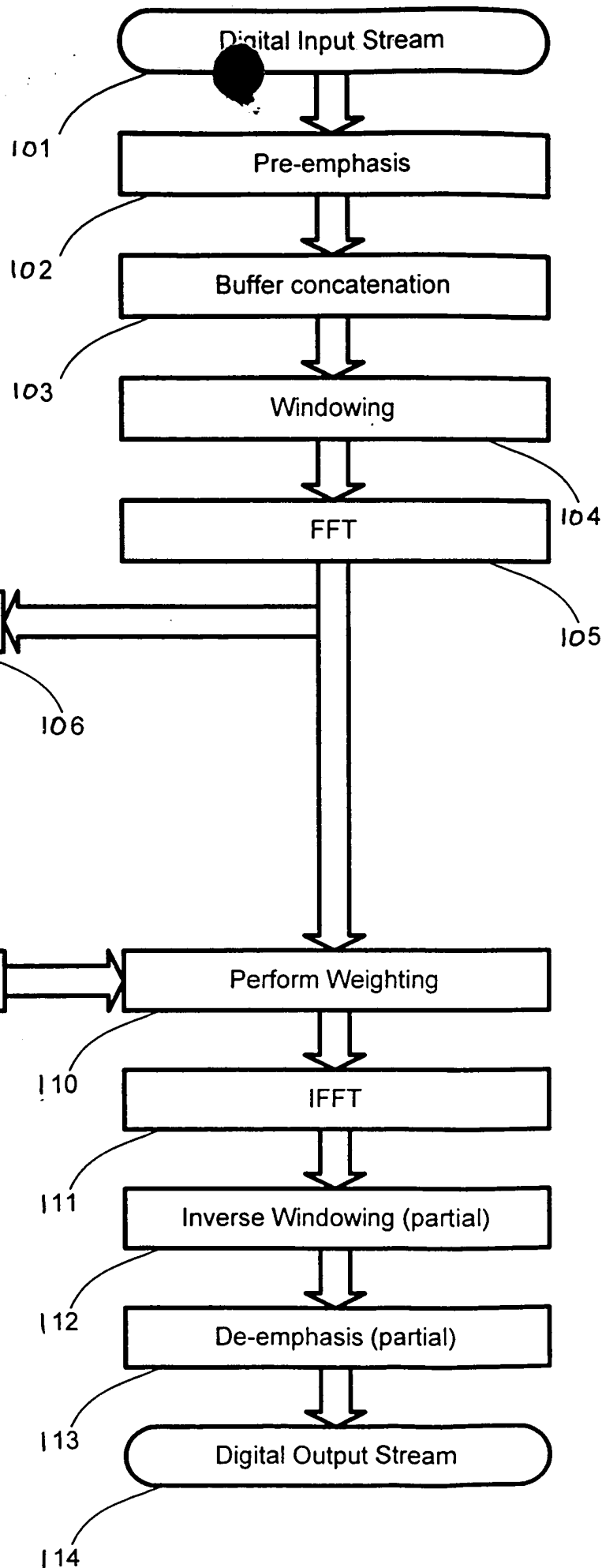


FIGURE 1

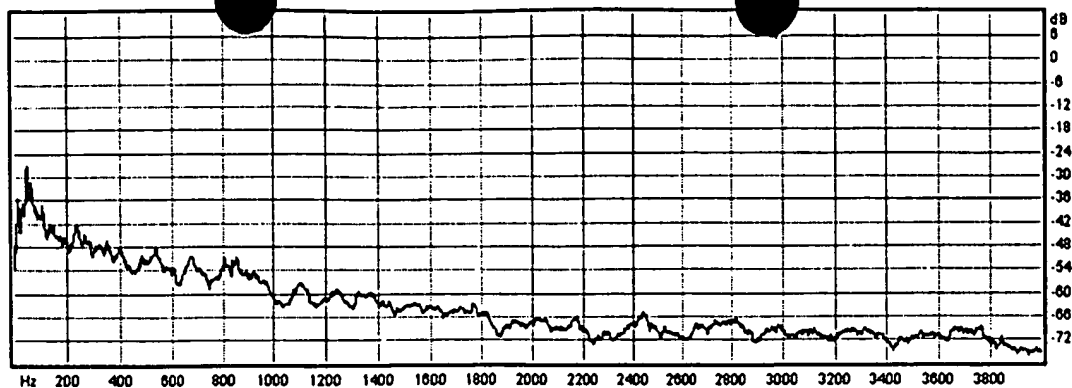


Fig 2a: Frequency plot of large sample of speech and noise.

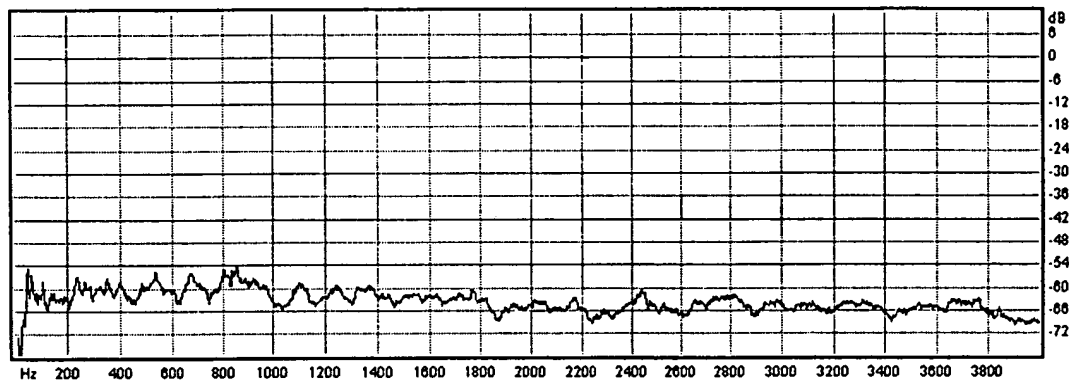


Fig 2b: Frequency plot of large sample of speech and noise after emphasis function has been applied.

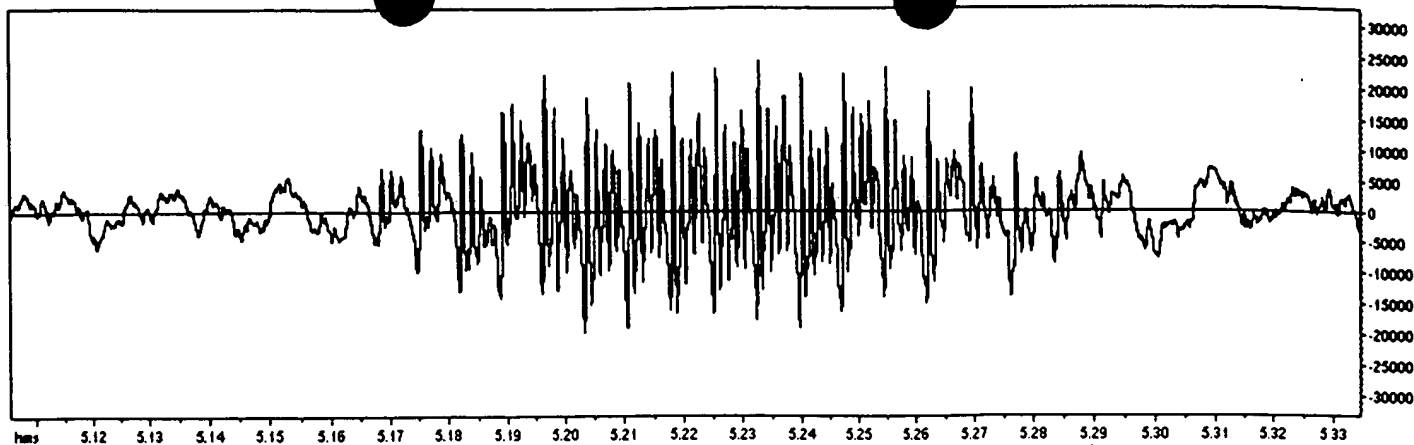


Fig 3a: Time domain plot of short sample of speech and noise.

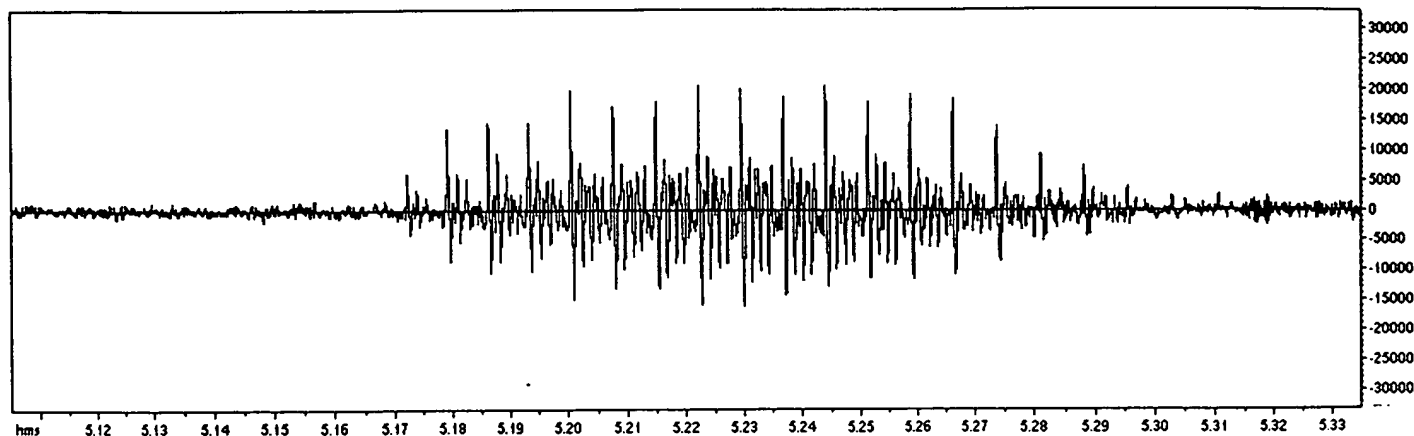
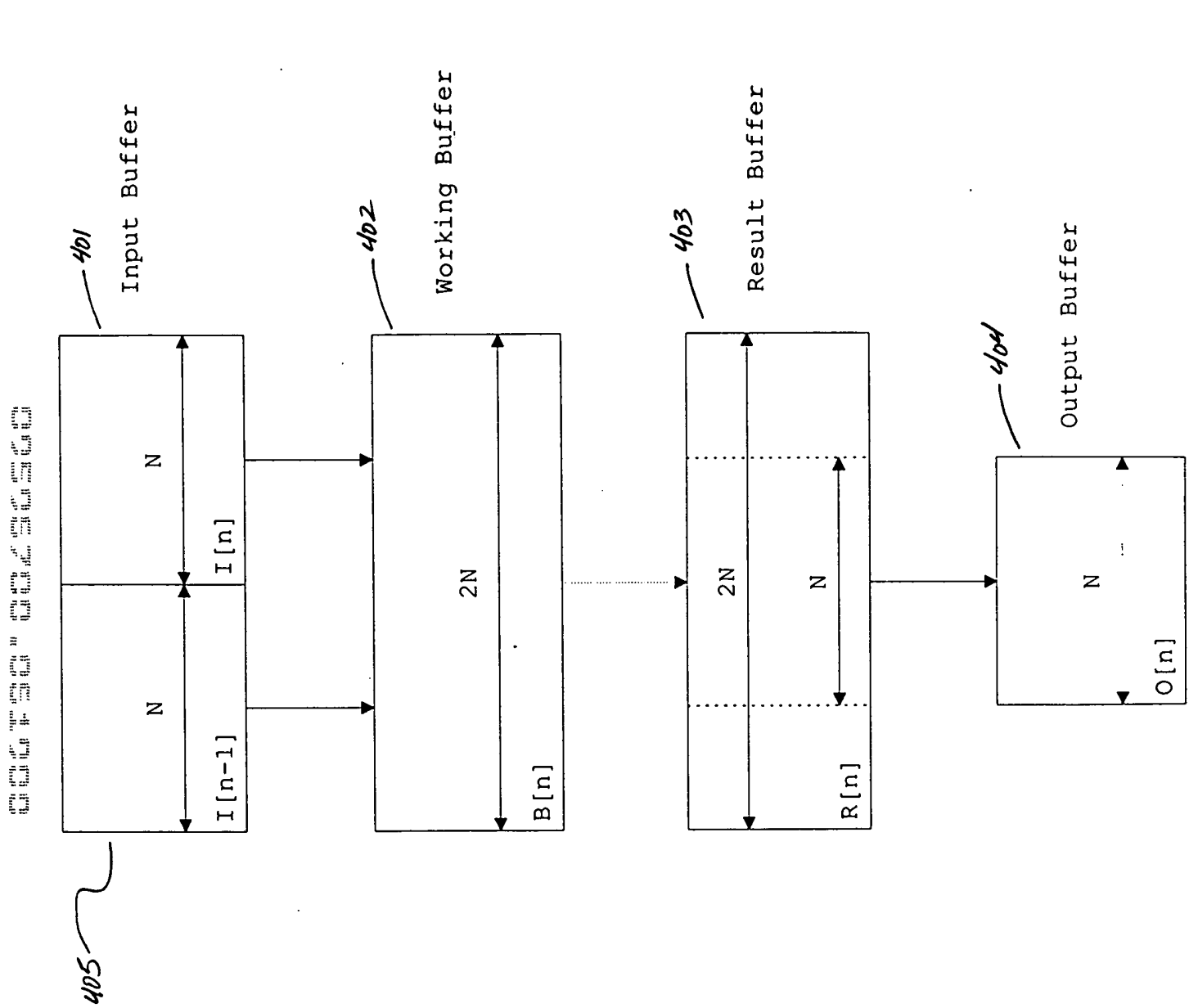


Fig 3b: Time domain plot of short sample of speech and noise after emphasis function has been applied.



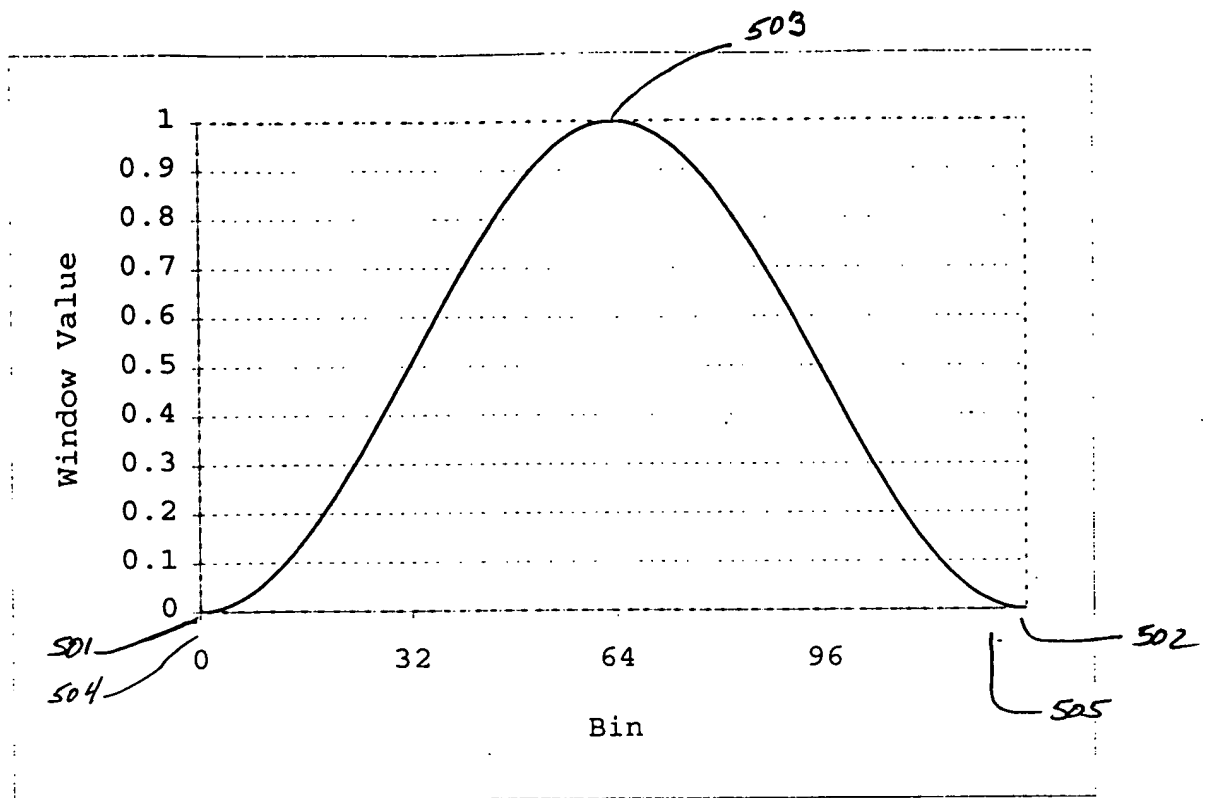


Fig 5a: Hanning Window Function

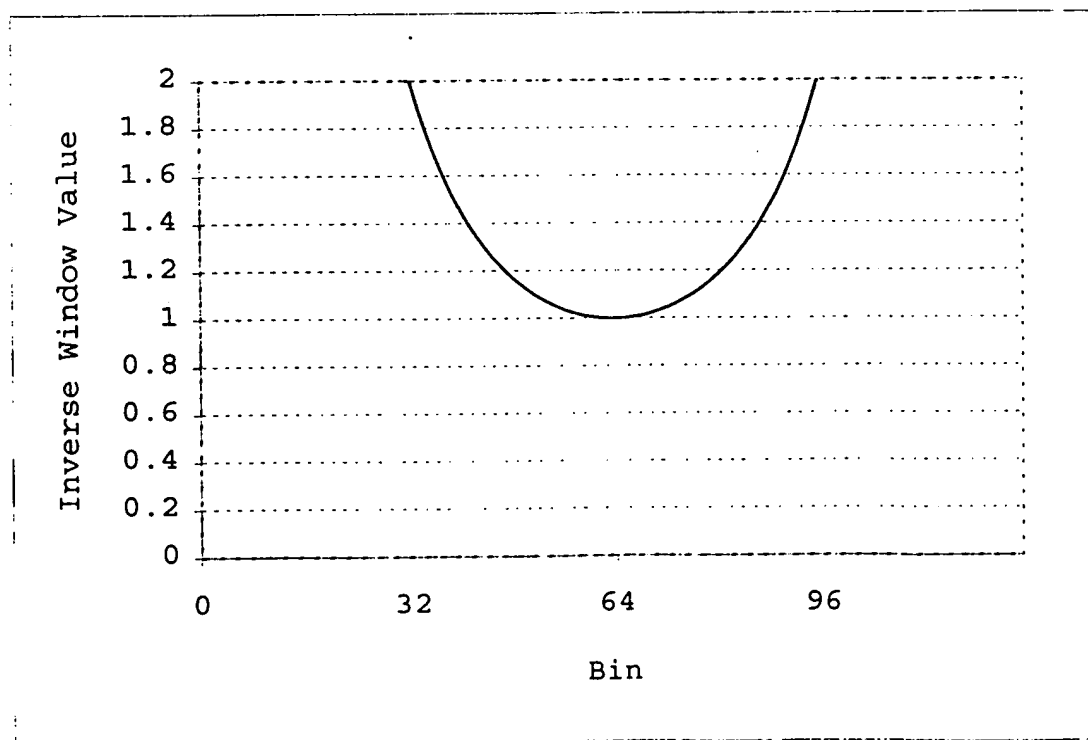


Fig 5b: Inverse Hanning Window Function

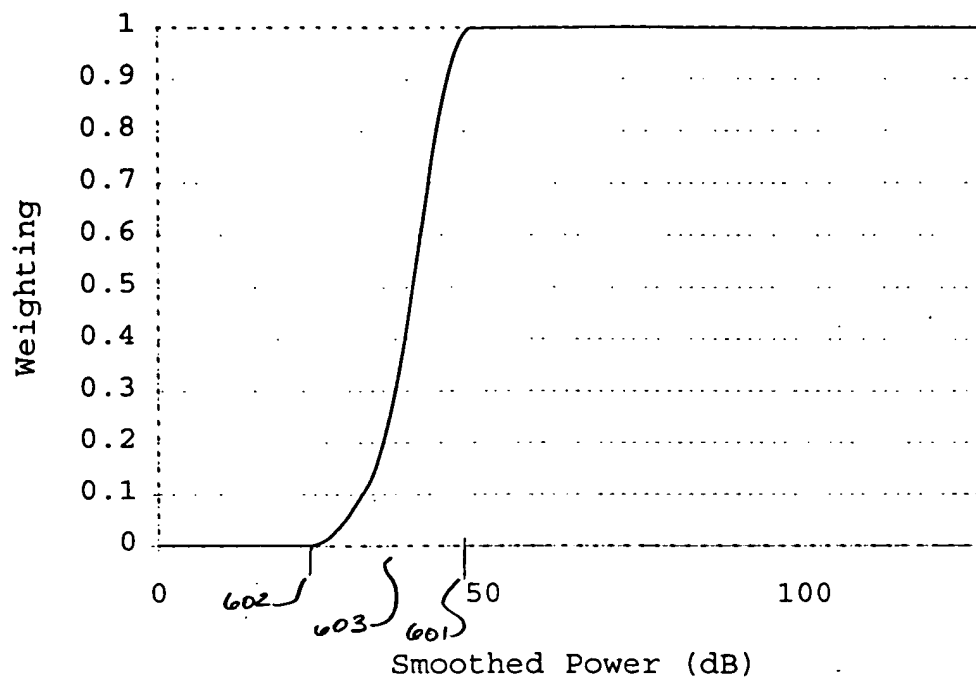
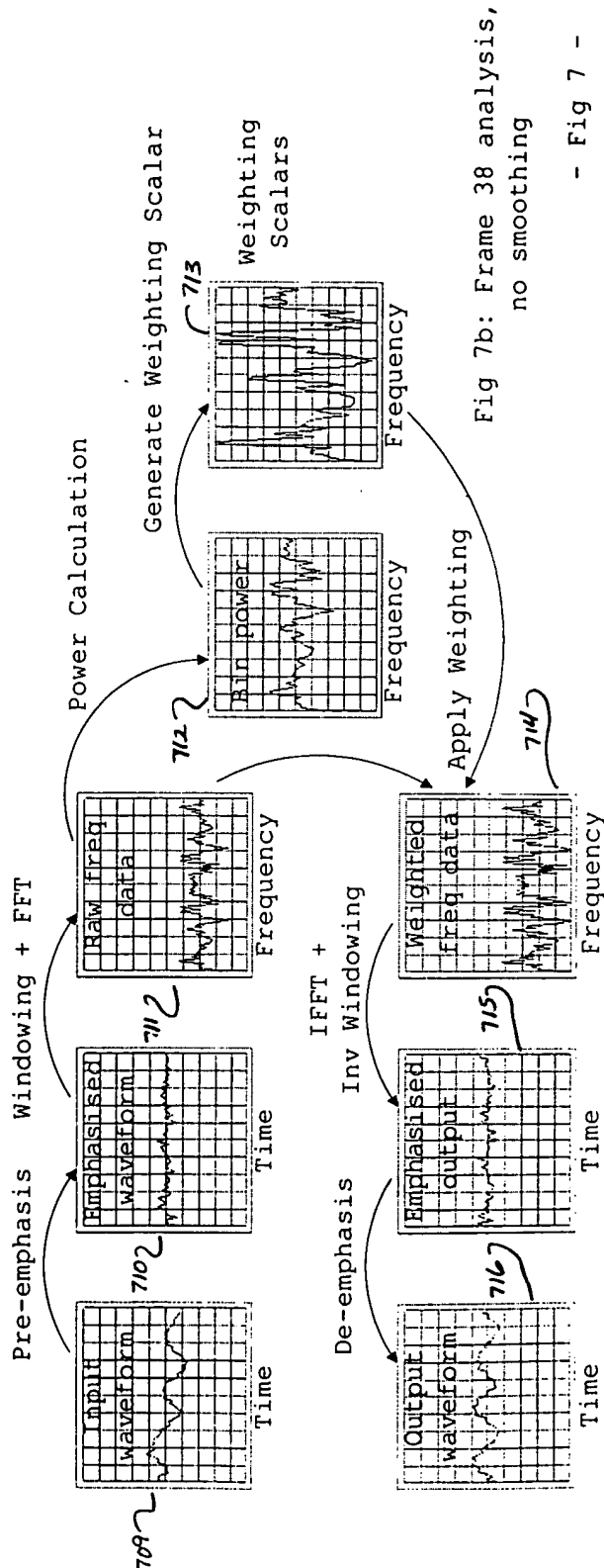
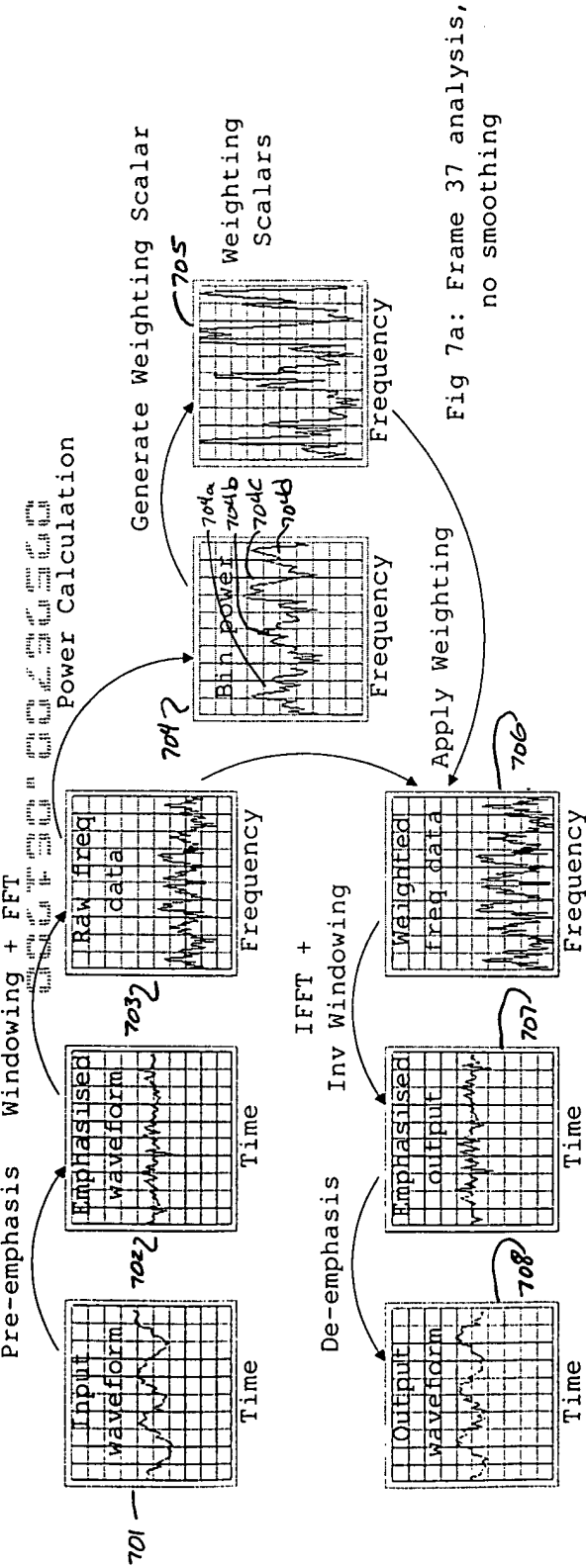


Fig 6: A Typical Weighting Function



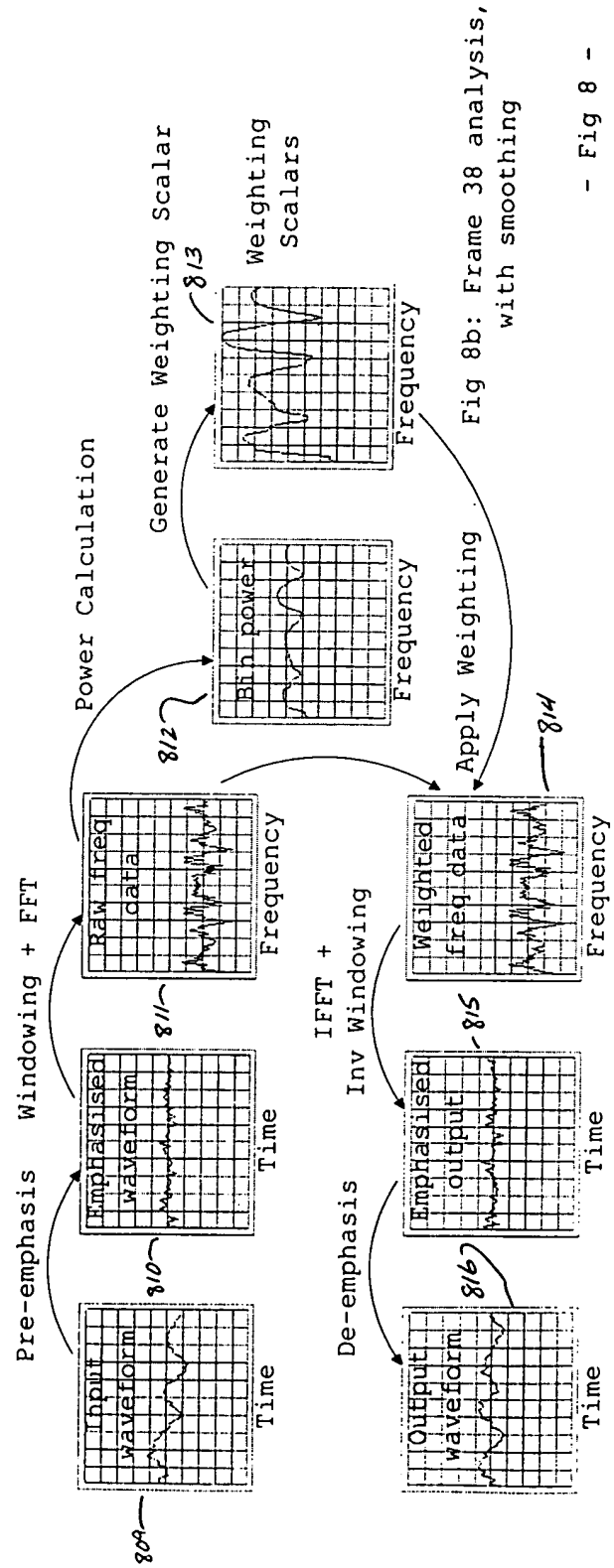
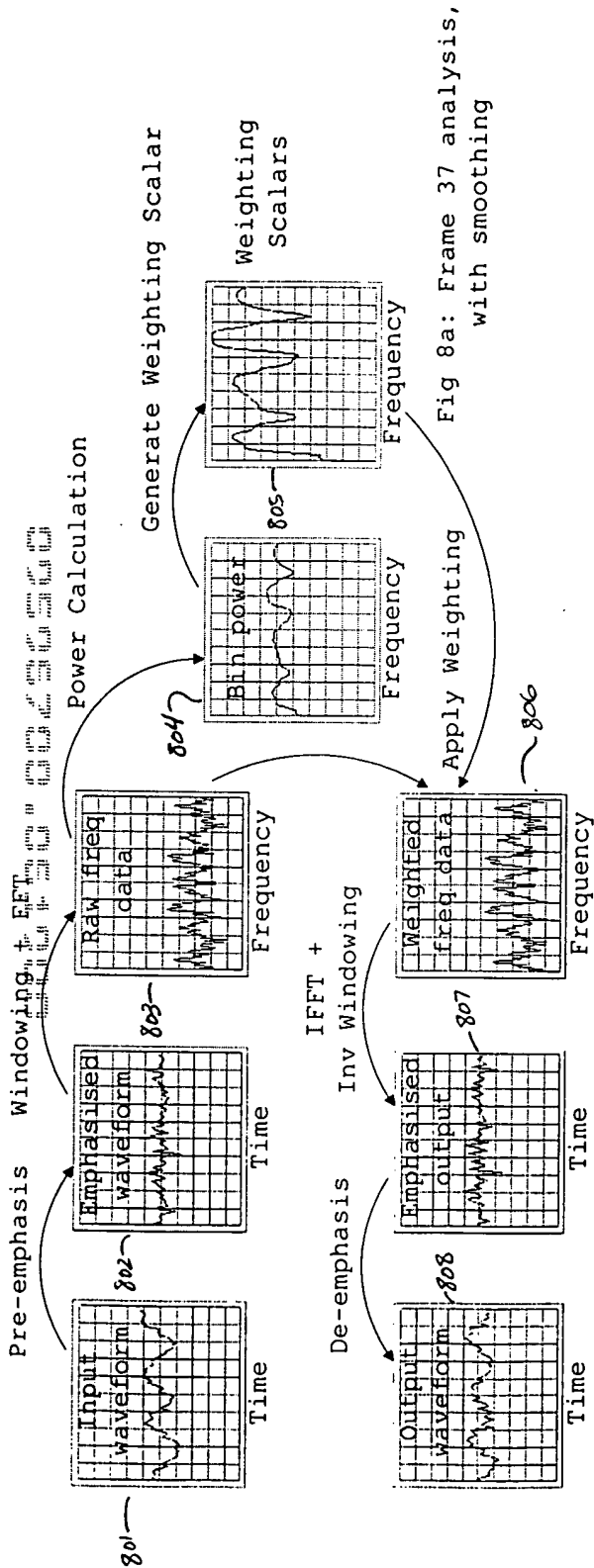




Fig 9a: Input signal

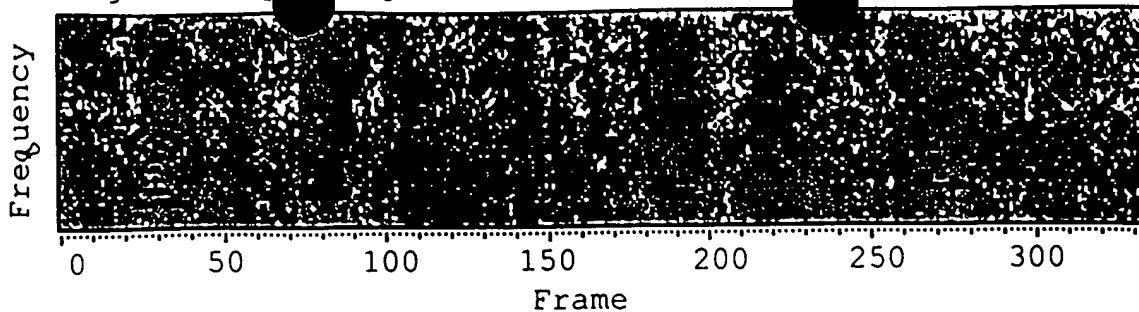


Fig 9b: Processed signal with no smoothing

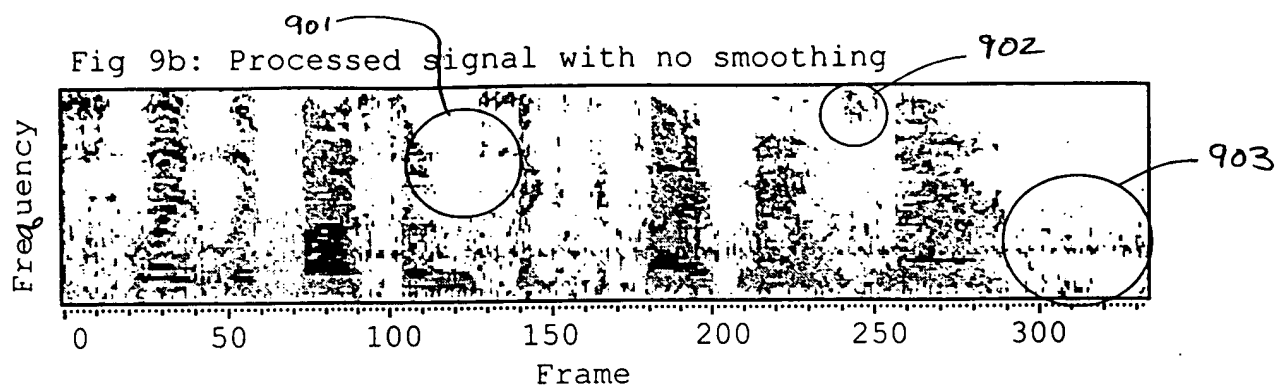


Fig 9c: Processed signal with temporal smoothing only

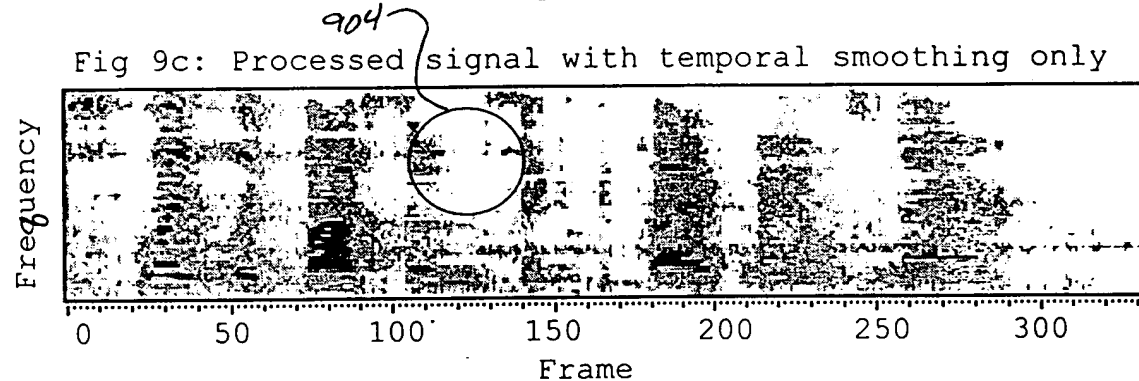


Fig 9d: Processed signal with transversal smoothing only

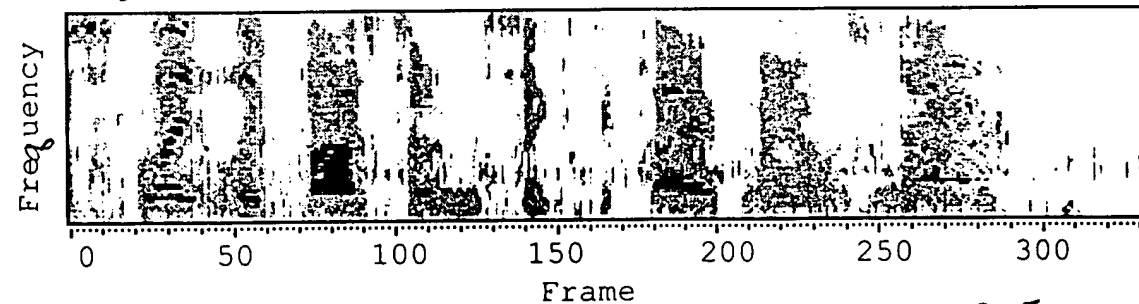
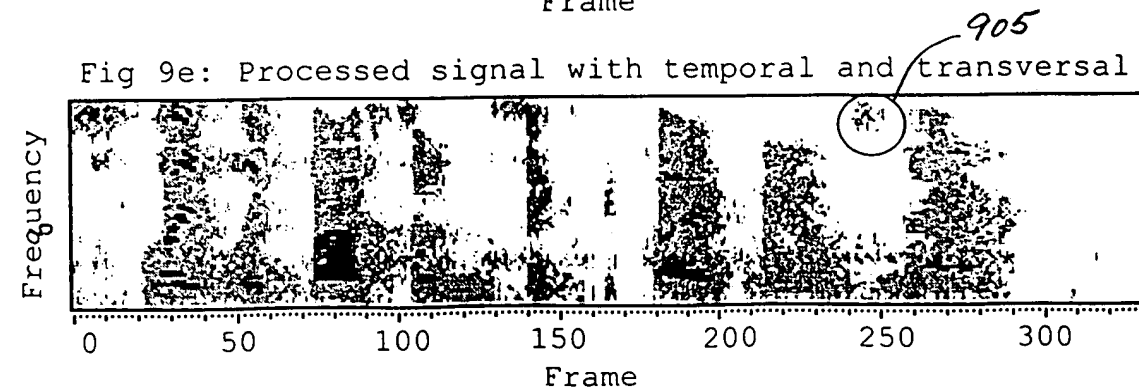
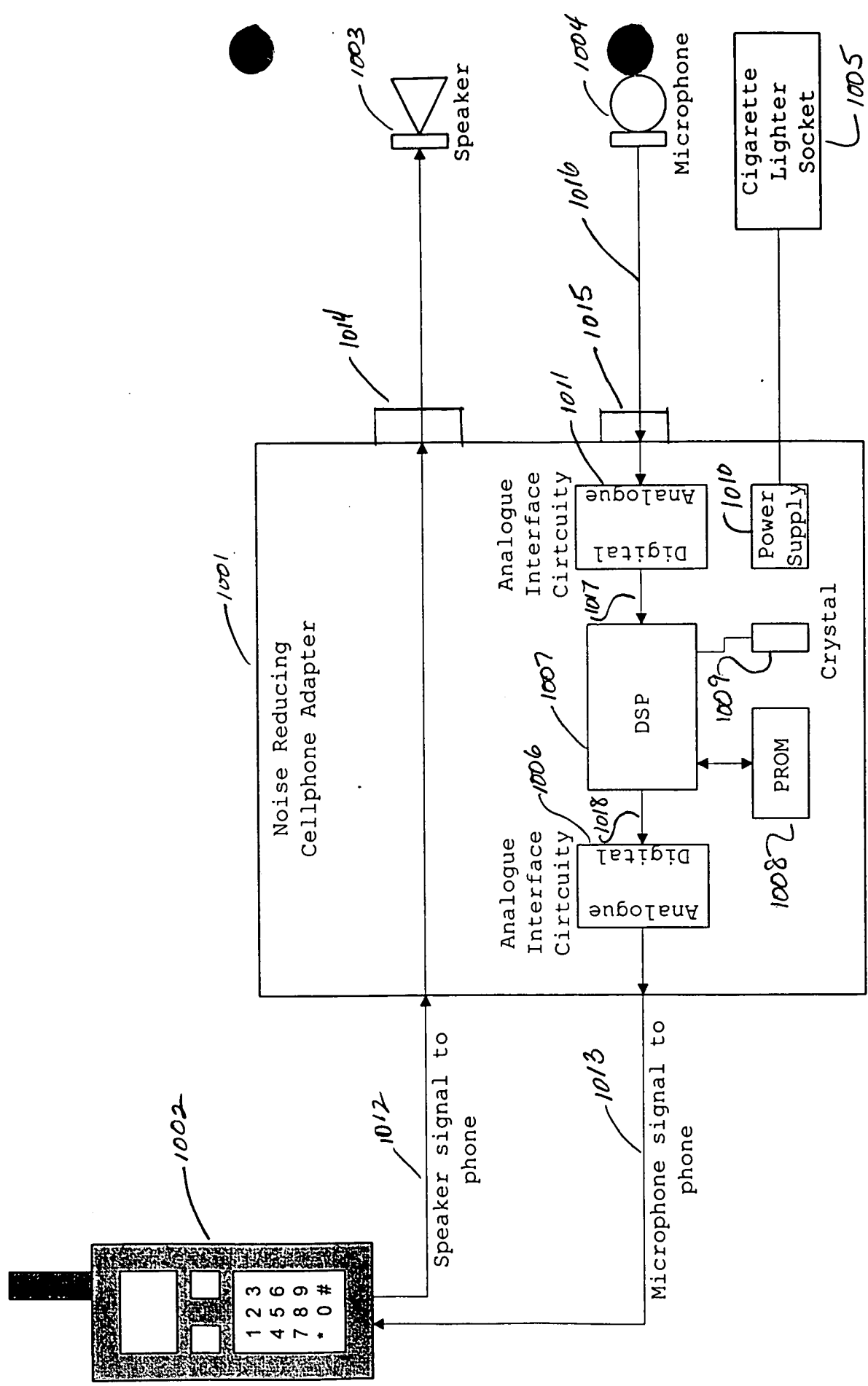


Fig 9e: Processed signal with temporal and transversal smoothing



any other type of noise reducing device may be used in place of the noise reducing adapter 1001.



- Fig 10 -